

REMARKS

Claims 2-13 and 15, 16 and 18-22 are pending in this application. By this amendment, Applicant cancels claim 17 and amends claim 20.

Upon a review of the Amendment filed on April 25, 2002, Applicant discovered that the amendment to claim 20 failed to include the features of intervening claim 17. Accordingly, Applicant has further amended allowable claim 20 to include the features of intervening claim 17. Accordingly, Applicant respectfully submits that claim 20, as amended in the Amendment filed on April 25, 2002 and further amended herein, is allowable as indicated by the Examiner.

In view of the foregoing amendments and remarks, Applicant respectfully submits that Claims 6, 7, 8, 13 and 20 are allowable over the prior art for the reasons described above. Claims 2-5, 9-12, 15, 16, 18, 19, 21 and 22 are dependent upon claims 8 and 20, and are therefore allowable for at least the reasons that claims 8 and 20 are allowable.

In view of the foregoing Remarks, Applicant respectfully submits that this Application is in condition for allowance. Favorable consideration and prompt allowance are respectfully solicited.

Serial No. 09/615,875
April 29, 2002
Page 4

The Commissioner is authorized to charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 50-1353.

Respectfully submitted,

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VERSION WITH MARKINGS SHOWING CHANGES MADE

20. A monolithic circuit array including a delay line comprising:
a coil divided into at least three inductors; and
a plurality of insulating layers stacked on each other to define a monolithic laminated body, the laminated body including at least three stages of low pass filters defined by lumped constant inductors and capacitors; wherein
the inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body; and
a ratio of a vertical dimension to a lateral dimension of each of the coil conductor patterns is approximately 1.